



## Description

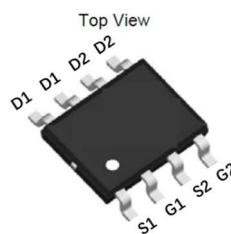
### JMT N And P-Channel Enhancement Mode MOSFET

#### Features

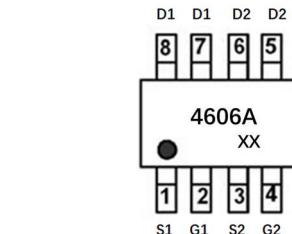
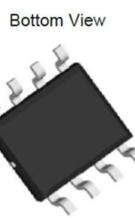
- N-Channel: 30V, 6A
  - $R_{DS(ON)} < 22m\Omega$  @  $V_{GS} = 10V$
  - $R_{DS(ON)} < 37m\Omega$  @  $V_{GS} = 4.5V$
- P-Channel: -30V, -6A
  - $R_{DS(ON)} < 35m\Omega$  @  $V_{GS} = -10V$
  - $R_{DS(ON)} < 50m\Omega$  @  $V_{GS} = -4.5V$
- Excellent Gate Charge x  $R_{DS(ON)}$  Product(FOM)
- Very Low On-resistance  $R_{DS(ON)}$
- Fast Switching Speed

#### Application

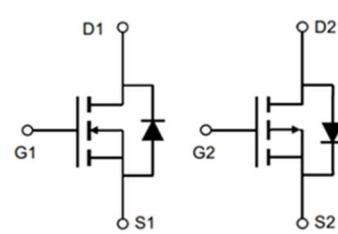
- Battery Protection
- Load Switch
- Power Management



SOP-8



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
4606	JMTP4606A	TAPING	SOP-8	13inch	4000	64000

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max. N-Channel	Max. P-Channel	Units
$V_{DSS}$	Drain-Source Voltage		30	-30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	$\pm 20$	V
$I_D$	Continuous Drain Current		$T_A = 25^\circ C$	6	-6
			$T_A = 100^\circ C$	4	-4
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		24	-24	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	1.4	2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		89	62.5	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150		$^\circ C$

**N-Channel Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}$ , $V_{GS}=0\text{V}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	1.55	3.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=6\text{A}$	-	18.6	22	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=5\text{A}$	-	30	37	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=20\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	255	310	pF
$C_{oss}$	Output Capacitance		-	45	60	pF
$C_{rss}$	Reverse Transfer Capacitance		-	35	50	pF
$Q_g$	Total Gate Charge	$V_{DS}=20\text{V}$ , $I_D=3\text{A}$ , $V_{GS}=10\text{V}$	-	5.2	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.0	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15\text{V}$ , $I_D=6\text{A}$ , $R_L=2.5\Omega$ , $R_{REN}=3\Omega$	-	4.5	-	ns
$t_r$	Turn-on Rise Time		-	2.5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	14.5	-	ns
$t_f$	Turn-off Fall Time		-	3.5	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	6	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	24	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=6\text{A}$	-	0.8	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

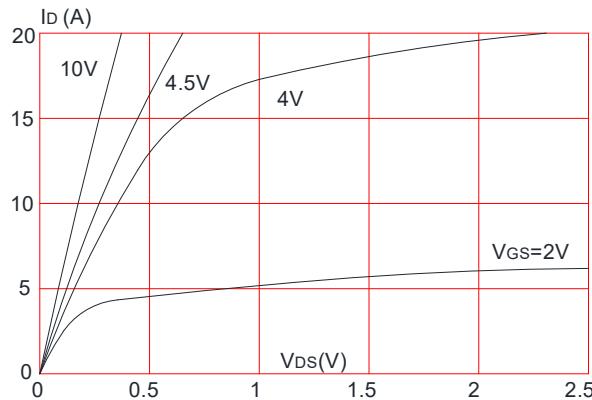
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 2\%$

**P-Channel Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

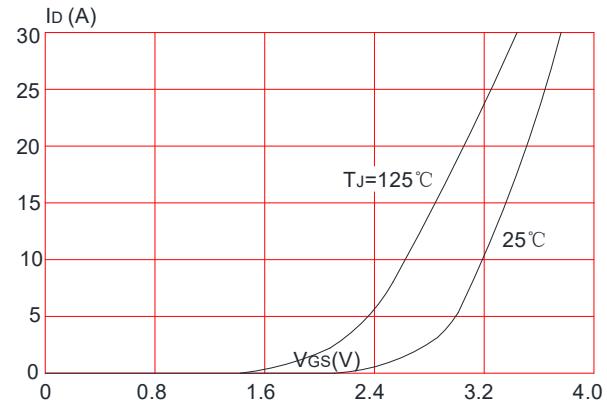
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=-30\text{V}$ , $V_{GS}=0\text{V}$	-	-	-1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm20\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$	-0.8	-1.32	-2.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}=-10\text{V}$ , $I_D=-6\text{A}$	-	27	35	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=-5\text{A}$	-	42	50	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=-20\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	760	-	pF
$C_{oss}$	Output Capacitance		-	140	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	95	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=-15\text{V}$ , $I_D=-3\text{A}$ , $V_{GS}=-10\text{V}$	-	13.6	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	3.2	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=-15\text{V}$ , $I_D=-6\text{A}$ , $V_{GS}=-10\text{V}$ , $R_{\text{GEN}}=2.5\Omega$	-	11	-	ns
$t_r$	Turn-on Rise Time		-	35	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	30	-	ns
$t_f$	Turn-off Fall Time		-	10	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-6	A	
$I_{sM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-24	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s=-6\text{A}$	-	-0.8	-1.2	V

## Typical Performance Characteristics-N

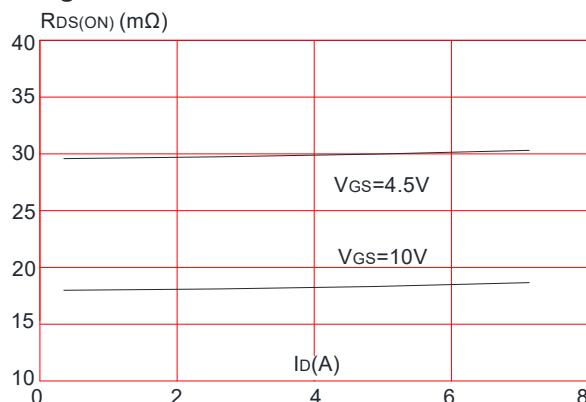
**Figure 1:** Output Characteristics



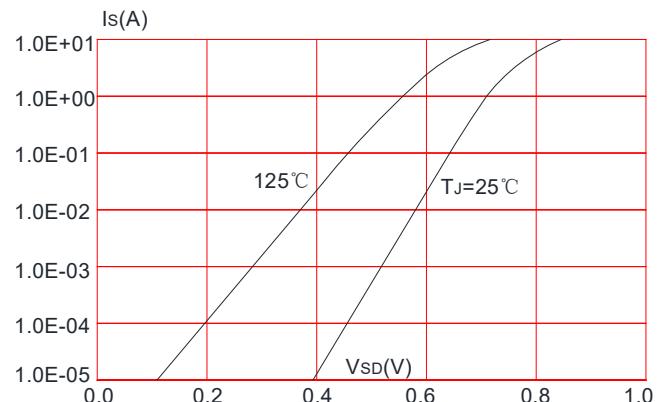
**Figure 2:** Typical Transfer Characteristics



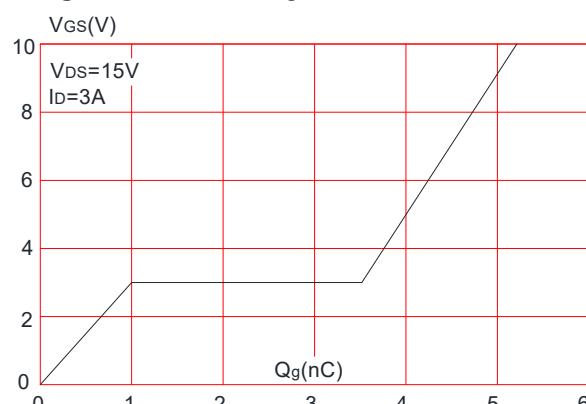
**Figure 3:** On-resistance vs. Drain Current



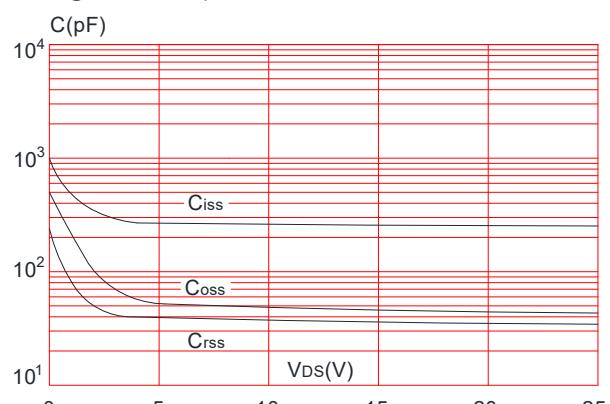
**Figure 4:** Body Diode Characteristics



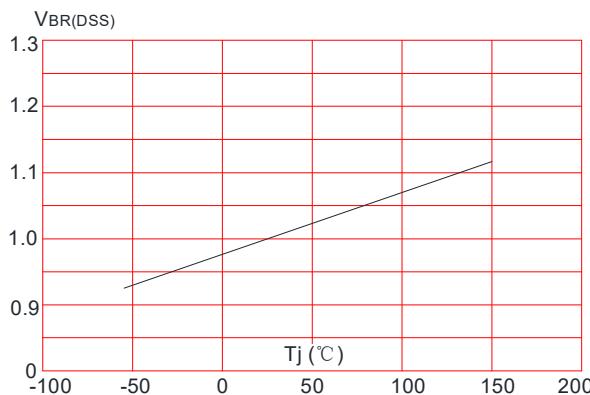
**Figure 5:** Gate Charge Characteristics



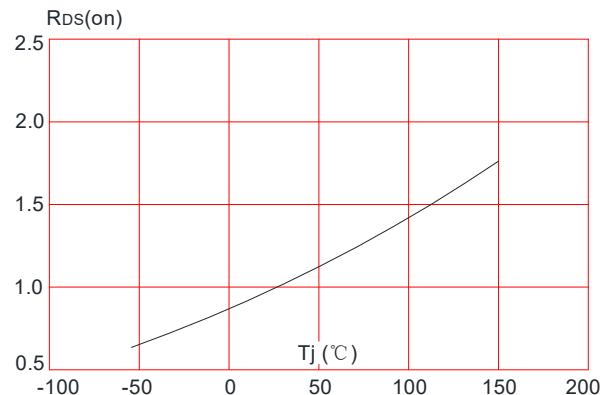
**Figure 6:** Capacitance Characteristics



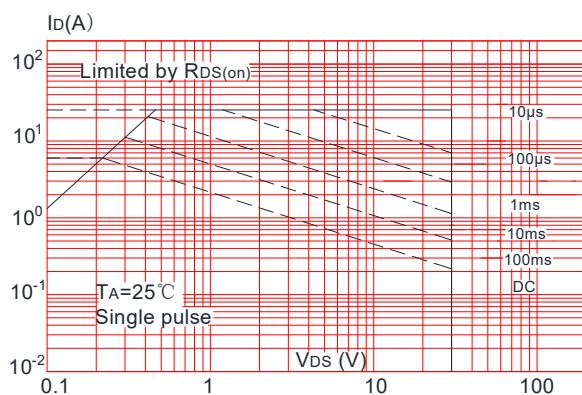
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



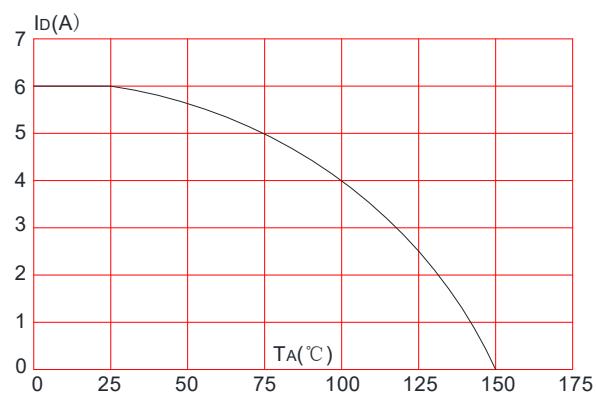
**Figure 8:** Normalized on Resistance vs. Junction Temperature



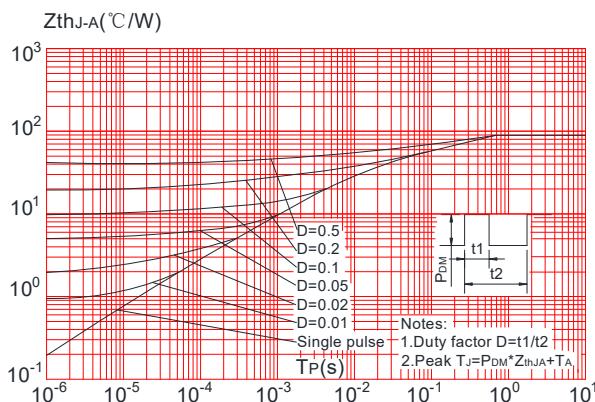
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## Test Circuit-N

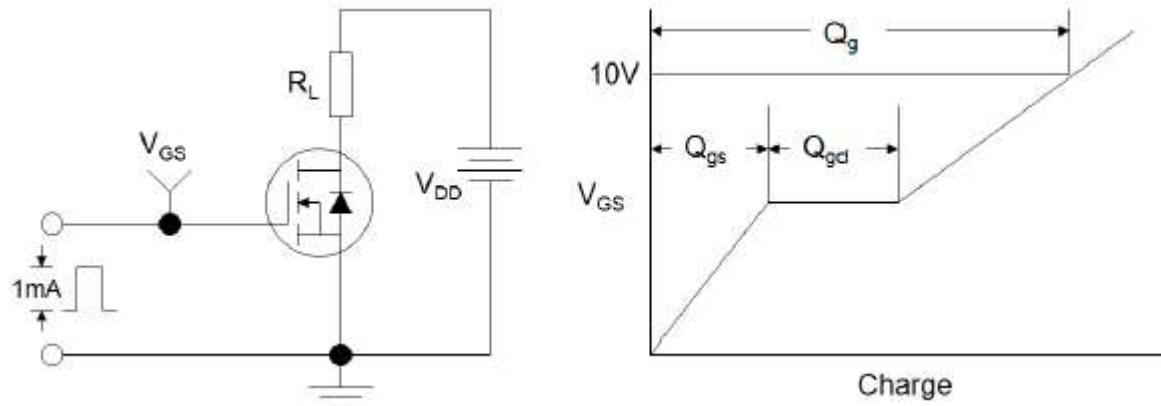


Figure1:Gate Charge Test Circuit & Waveform

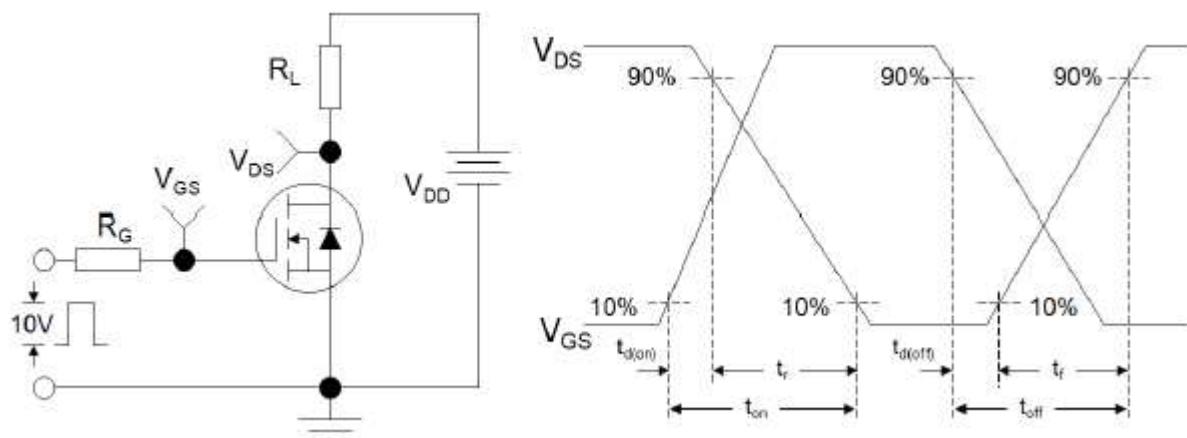


Figure 2: Resistive Switching Test Circuit & Waveforms

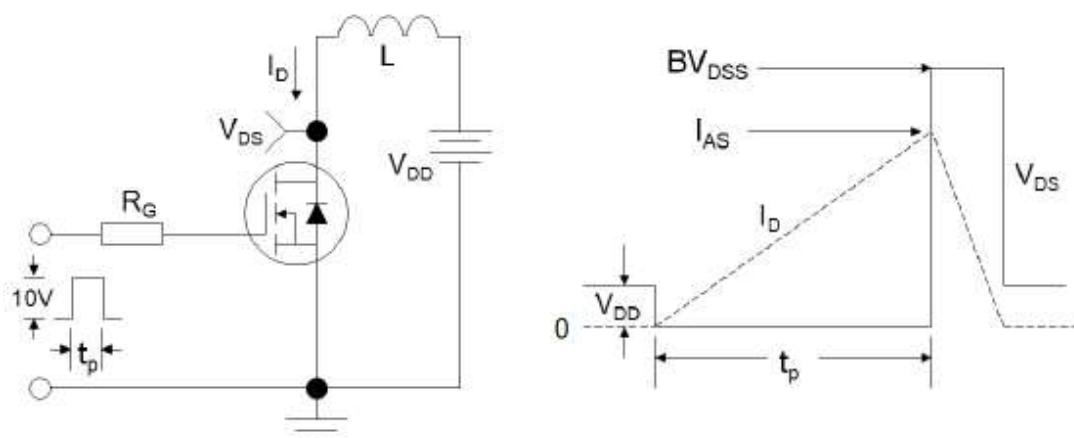
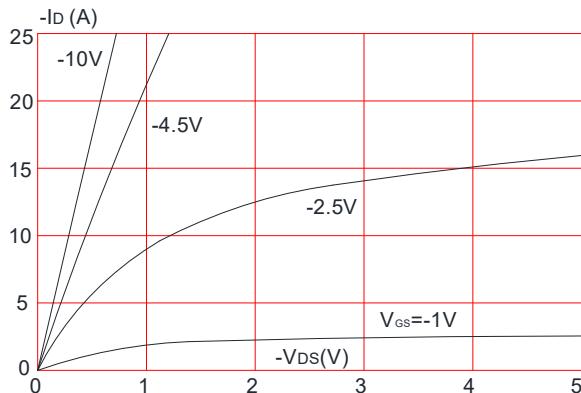


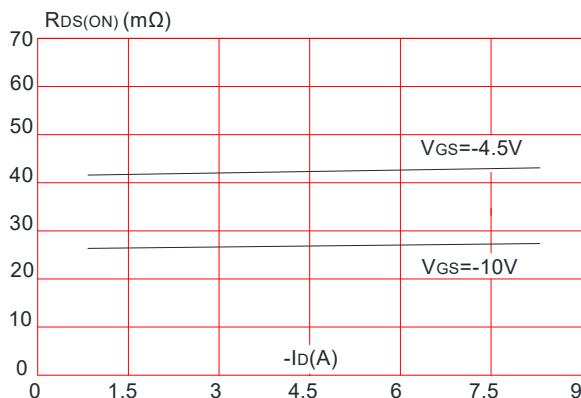
Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

## Typical Performance Characteristics-P

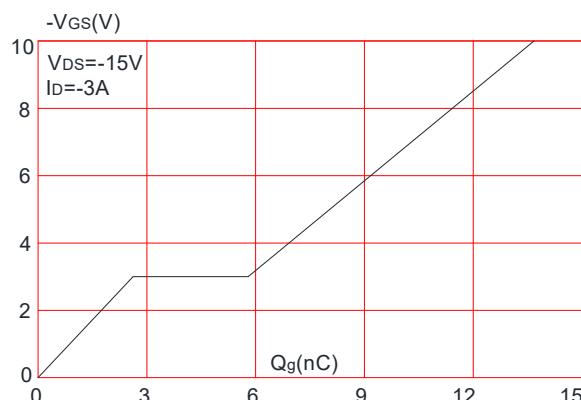
**Figure 1:** Output Characteristics



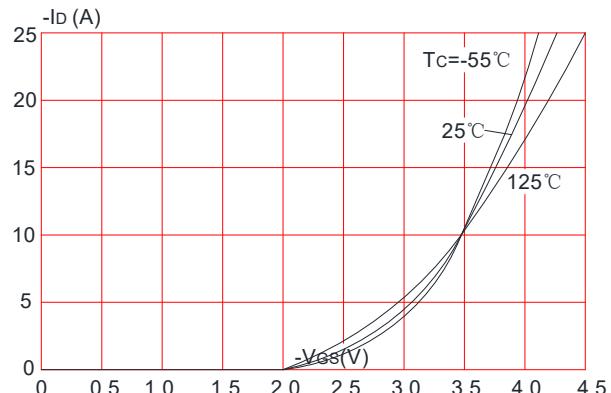
**Figure 3:** On-resistance vs. Drain Current



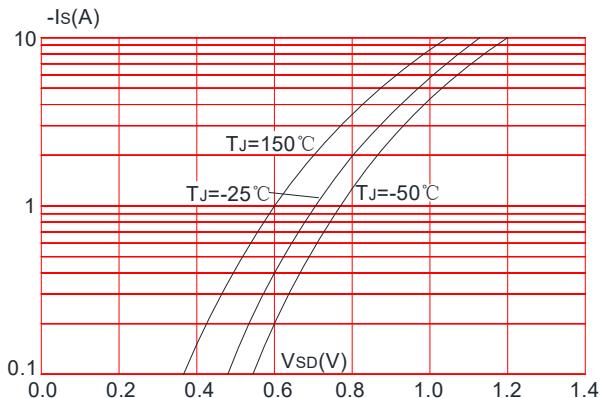
**Figure 5:** Gate Charge Characteristics



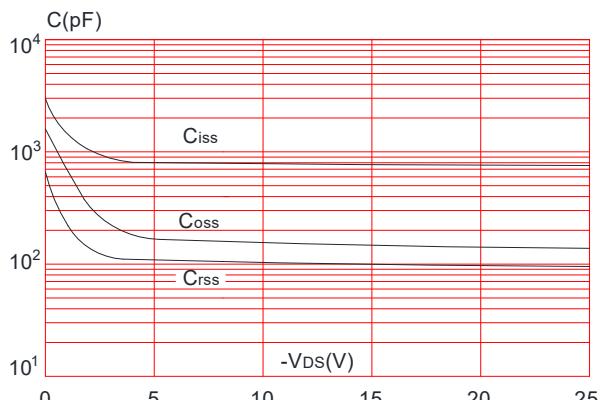
**Figure 2:** Typical Transfer Characteristics



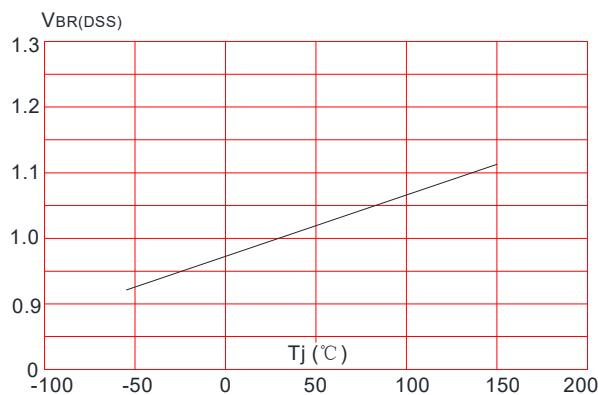
**Figure 4:** Body Diode Characteristics



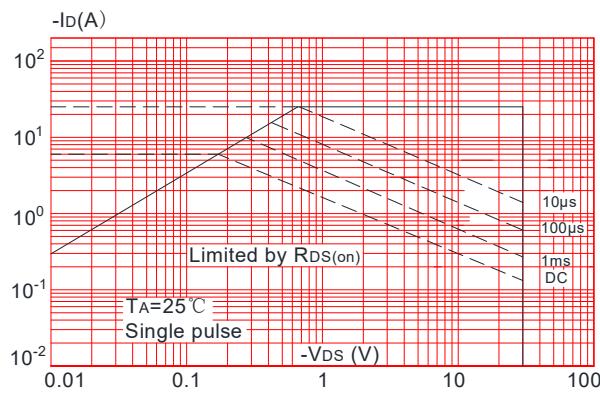
**Figure 6:** Capacitance Characteristics



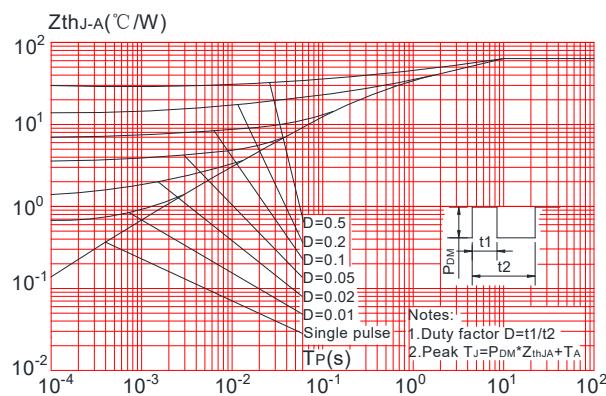
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



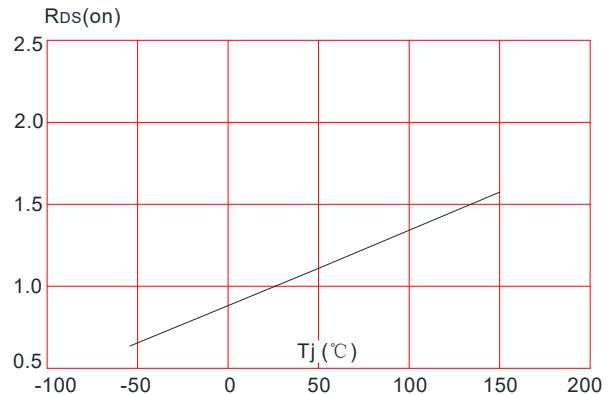
**Figure 9:** Maximum Safe Operating Area



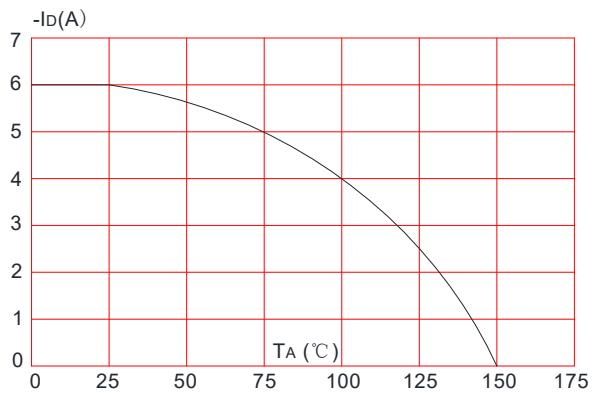
**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



**Figure 8:** Normalized on Resistance vs. Junction Temperature

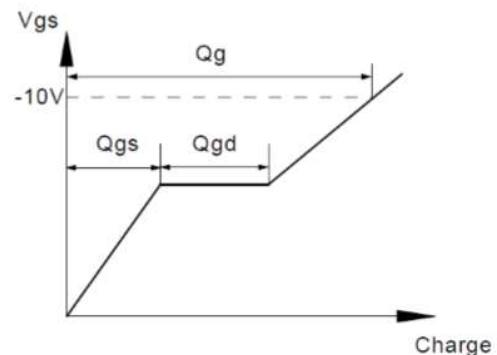
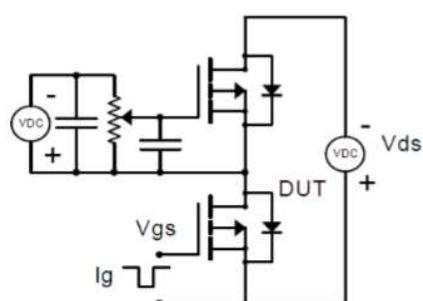


**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature

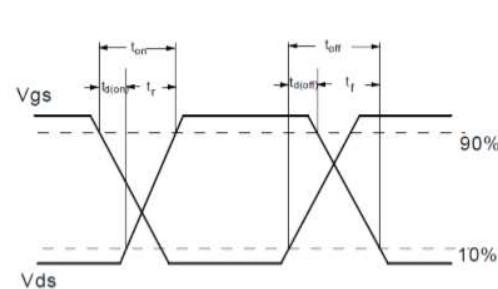
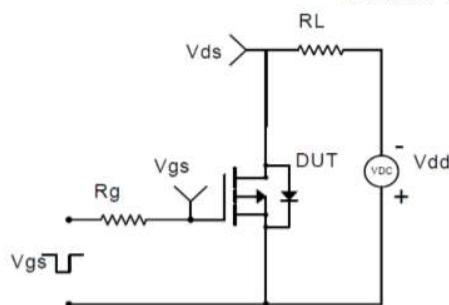


## Test Circuit-P

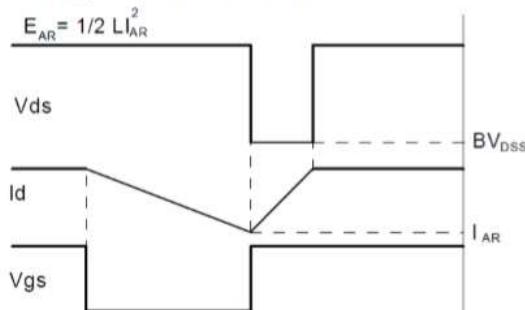
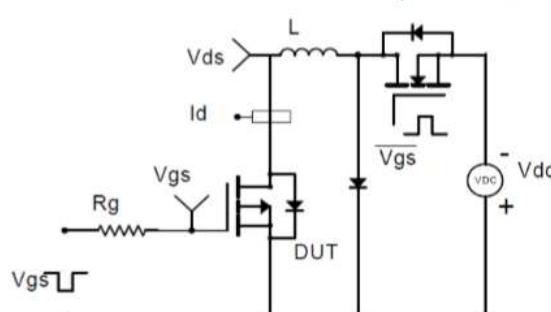
Gate Charge Test Circuit & Waveform



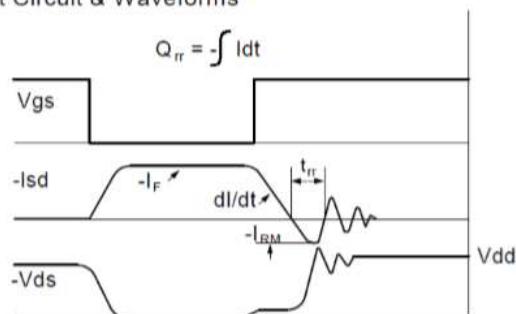
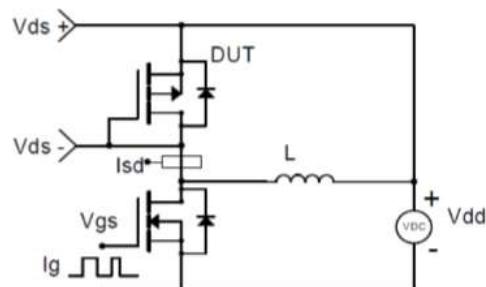
Resistive Switching Test Circuit & Waveforms



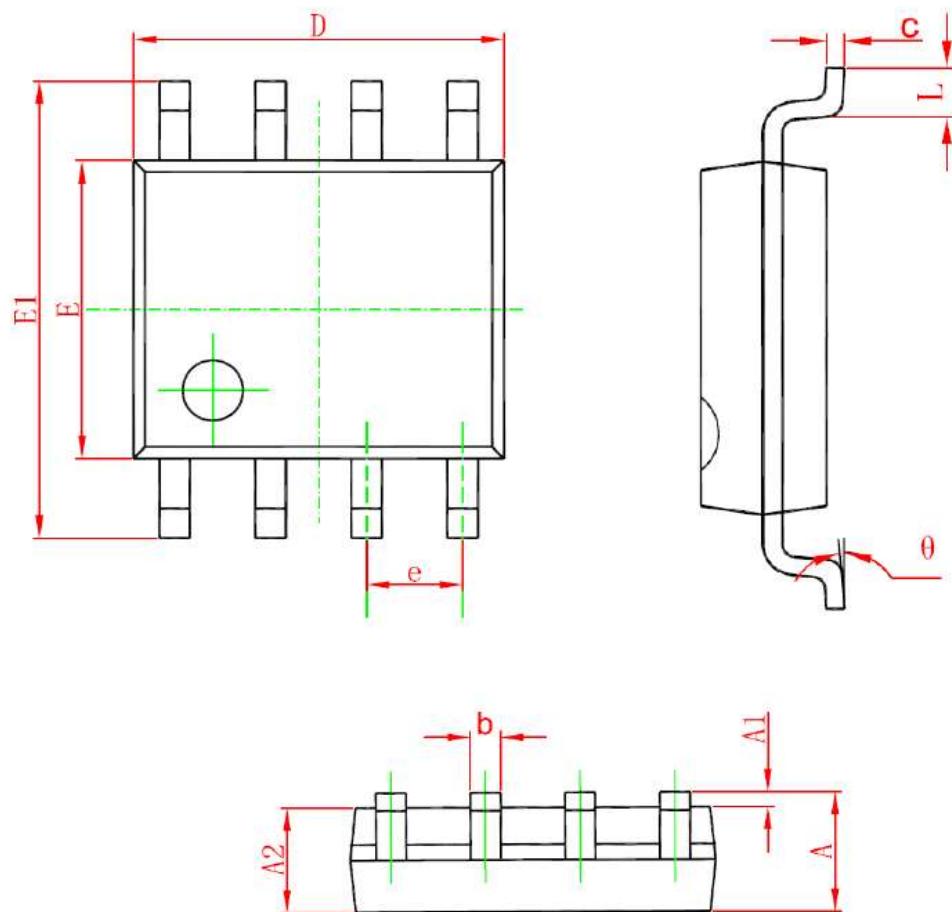
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°



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